

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title	49.0" WUXGA TFT LCD
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BUYER	
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC490EPY
SUFFIX	SHP1(RoHS verified)

APPROVED BY	SIGNATURE DATE
/	
/	
/	

APPROVED BY	SIGNATURE DATE
S.J. Lee / Team Leader	
REVIEWED BY	
/ Project Leader	
PREPARED BY	
/ Engineer	

Please return 1 copy for your confirmation with your signature and comments.

**TV Product Development Dept.
LG Display Co., Ltd.**

Product Specification

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
1.0	15.09.01	-	Final CAS

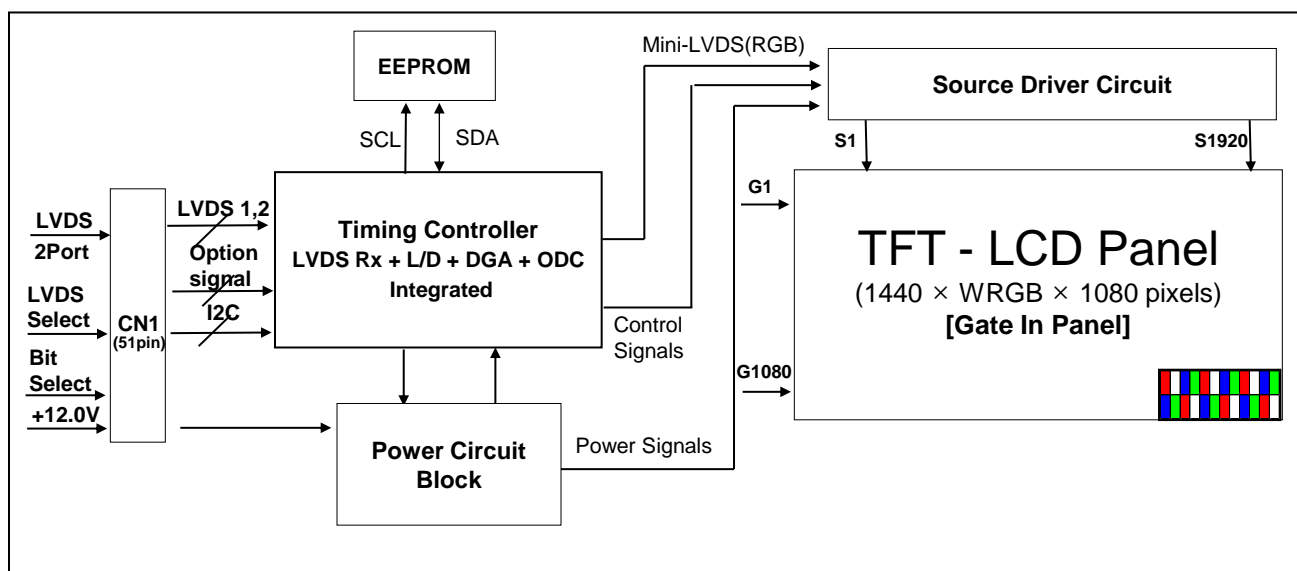
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1. General Description

The LD490EPY is a Color Active Matrix Liquid Crystal Display with an integral the Source PCB and Gate implanted on Panel (GIP). The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 48.5inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green, Blue and White sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10bit(D) gray scale signal for each dot. Therefore, it can present a palette of more than 1.07B colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	48.5 inches(1232.0mm) diagonal
Outline Dimension	1085.50(H) X 616.00(V) X 1.30mm(D)(Typ.)
Pixel Pitch	0.1864X3 [mm] × 0.5593 [mm]
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGBW stripe arrangement
Color Depth	10-bit(D), 1.07 Billion colors
Drive IC Data Interface	Source D-IC : 8-bit Mini-LVDS, gamma reference voltage, and control signals Gate D-IC : Gate In Panel
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Weight	2.6Kg (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment (Top)	Hard coating(2H), Anti-reflection treatment of the front polarizer (Reflectance < 2%)
Possible Display Type	Landscape and Portrait Enabled

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2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
			Min	Max		
Power Input Voltage	LCD Circuit	V _{LCD}	-0.3	+14.0	V _{DC}	1
T-Con Option Selection Voltage		V _{LOGIC}	-0.3	+4.0	V _{DC}	
Operating Temperature		T _{OP}	0	+50	°C	2,3
Storage Temperature(without packing)		T _{ST}	-20	+60	°C	
Panel Front Temperature		T _{SUR}	-	+68	°C	4
Operating Ambient Humidity		H _{OP}	10	90	%RH	2,3
Storage Humidity		H _{ST}	5	90	%RH	

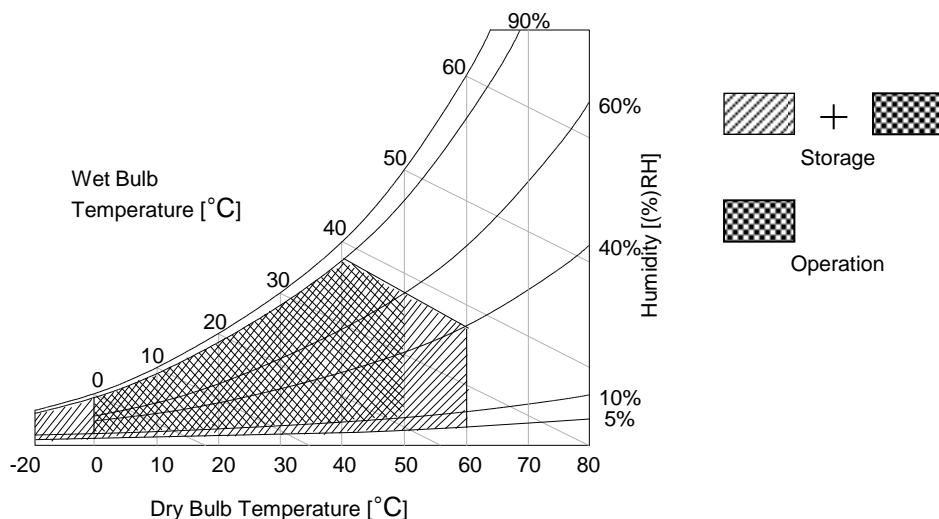
Note1. Ambient temperature condition ($T_a = 25 \pm 2 \text{ }^\circ\text{C}$)

2. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be Max 39°C, and no condensation of water.

3. Gravity mura can be guaranteed below 40°C condition.

4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



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3. Electrical Specifications

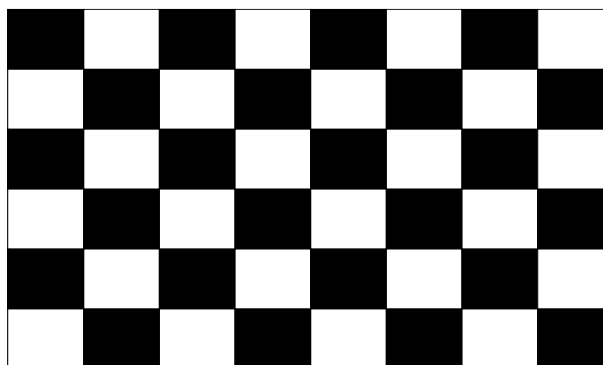
3-1. Electrical Characteristics

Table 2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Value			Unit	Note
			Min	Typ	Max		
Circuit :							
Power Input Voltage		V _{LCD}	10.8	12.0	14	VDC	5
Power Input Current		I _{LCD}	-	453	588	mA	1
			-	578	751	mA	2
T-CON Option Selection Voltage	Input High Voltage	V _{IH}	2.7	-	3.6	VDC	
	Input Low Voltage	V _{IL}	0	-	0.7	VDC	
Power Consumption		PLCD	-	5.4	7.1	Watt	1
Rush current		IRUSH	-	-	8	A	3

- Note 1. The specified current and power consumption are under the $V_{LCD}=12.0V$, $T_a=25 \pm 2^\circ C$, $f_v=60Hz$ condition, and mosaic pattern(8 x 6) is displayed and f_v is the frame frequency.
2. The current is specified at the maximum current pattern.
3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).
4. Ripple voltage level is recommended under $\pm 5\%$ of typical voltage
5. Maximum of Power Input Voltage is included with ripple.

White : 255 Gray
Black : 0 Gray



Mosaic Pattern(8 x 6)

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3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RXE51S-HF (manufactured by JAE) or compatible

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC(Reverse)	No Connection or Ground	27	Bit Select	'H' or Open = 10bit(D) , 'L' = 8bit
2	NC(MPLUS_ON)	No Connection	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or Open = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection	34	GND	Ground
9	NC	No Connection	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection or Ground	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	GND	Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	GND	Ground
18	GND	Ground	44	AGP or NSB	'H' or Open= AGP, 'L'= NSB(No signal Black)
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	GND	Ground	-	-	-

- Note
1. All GND(ground) pins should be connected together to the LCD module's metal frame.
 2. All VLCD (power input) pins should be connected together.
 3. All Input levels of LVDS signals are based on the **EIA 644** Standard.
 4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)
 5. Specific pin No. #44 is used for "No signal detection" of system signal interface.
It should be GND for NSB(No Signal Black) during the system interface signal is not.
If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

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3-3. Signal Timing Specifications

Table 4 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 4. TIMING TABLE for NTSC & PAL (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	notes
Horizontal	Display Period	t _{HV}	960	960	960	tCLK	1920 / 2
	Blank	t _{HB}	100	140	240	tCLK	1
	Total	t _{HP}	1060	1100	1200	tCLK	
Vertical	Display Period	t _{VV}	1080	1080	1080	Lines	
	Blank	t _{VB}	20	45	300	Lines	1
	Total	t _{VP}	1100	1125	1380	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	notes
Frequency	DCLK	f _{CLK}	60.00	74.25	78.00	MHz	
	Horizontal	f _H	57.3	67.5	70	KHz	2
	Vertical	f _V	47	60	63	Hz	2

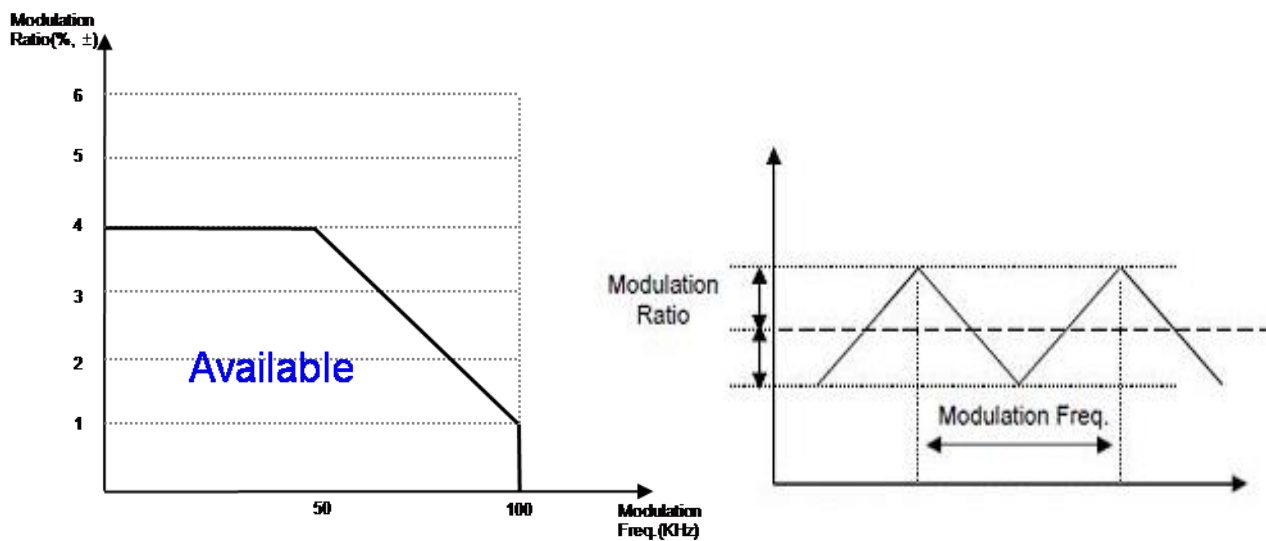
Note 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode).
If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency

3. Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by $(7 - 0.06 \cdot F_{mod})$, where Modulation Frequency (F_{MOD}) unit is KHz.
LVDS Receiver Spread spectrum Clock is defined as below figure

※ Timing should be set based on clock frequency.

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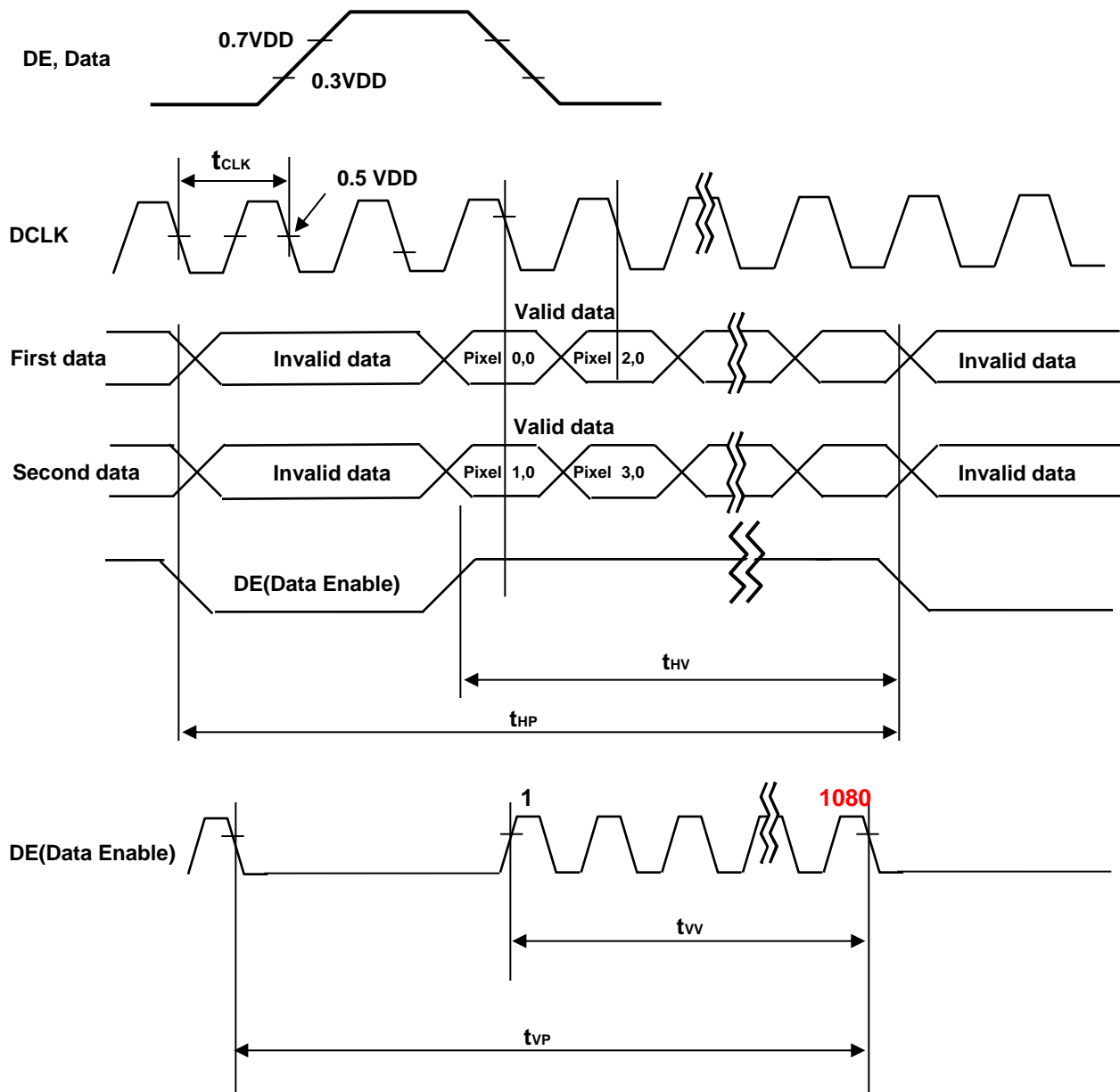
※ Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD)

1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
2. Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.

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3-4. LVDS Signal Specification

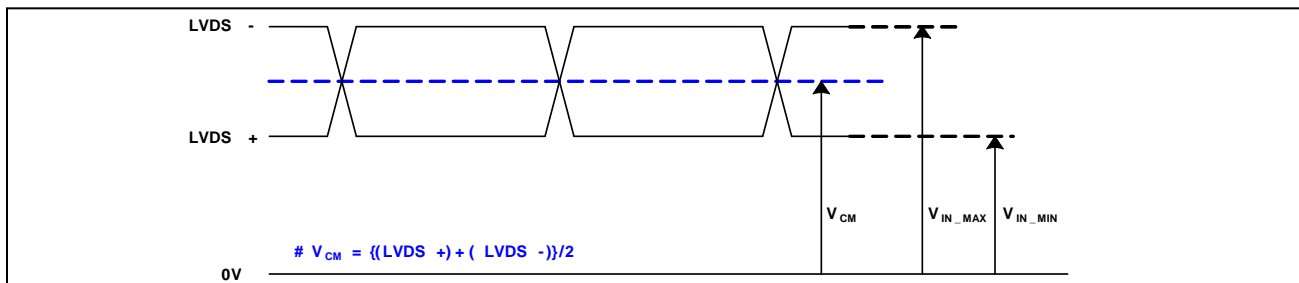
3-4-1. LVDS Input Signal Timing Diagram



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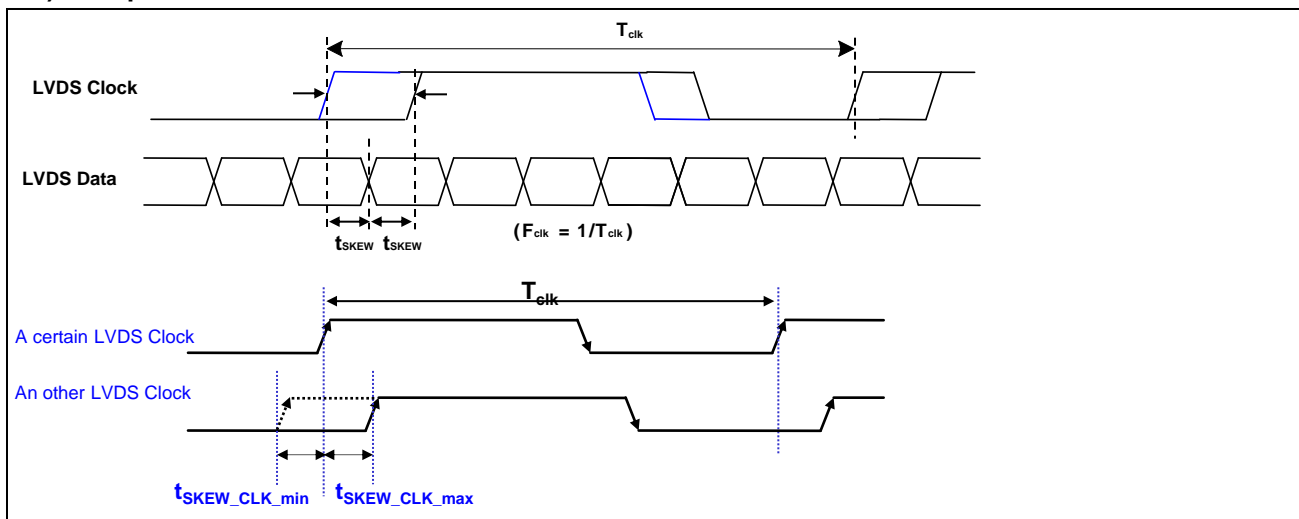
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	notes
LVDS Common mode Voltage	V_{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V_{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔV_{CM}	-	250	mV	-

2) AC Specification

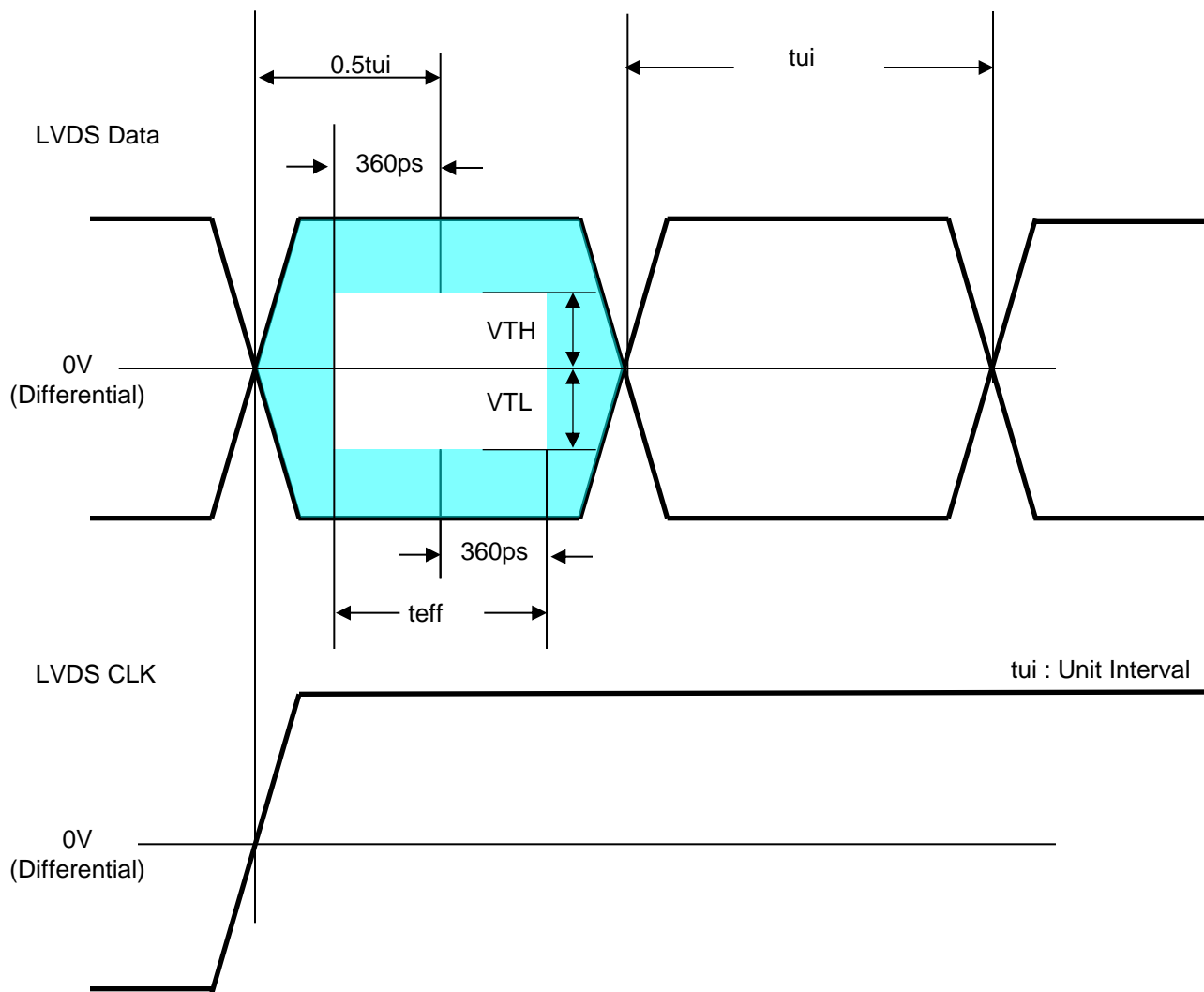


Description	Symbol	Min	Max	Unit	notes
LVDS Differential Voltage	V_{TH}	100	600	mV	Tested with Differential Probe 2
	V_{TL}	-600	-100	mV	
LVDS Clock to Data Skew	t_{SKEW}	-	$ (0.2 * T_{clk})/7 $	ps	-
Effective time of LVDS	t_{eff}	$ \pm 360 $	-	ps	-
LVDS Clock to Clock Skew (Each other)	t_{SKEW_CLK}	-	$ 1/7 * T_{clk} $	ps	-

Notes 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

2. LVDS Differential Voltage is defined within t_{eff}

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* This accumulated waveform is tested with differential probe

3-5. Intra interface Signal Specification

3-5-1. Mini-LVDS Signal Specification

Table 5-1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	MIN	TYP	MAX	Unit	Note
Mini-LVDS input Voltage (Center)	V_{IB}	Mini-LVDS Clock and Data	$0.7+VID/2$	-	$(VCC-1.2) - VID / 2$	V	1
Mini-LVDS input Voltage Distortion (Center)	ΔV_{IB}		-	-	0.8	V	
Mini-LVDS differential Voltage range	V_{ID}		180	-	700	mV	
Mini-LVDS differential Voltage range Dip	ΔV_{ID}		25	-	700	mV	2
Mini Clock pulse period	T_1		3.2	6.7			
Mini Clock pulse high period	T_2		1.6				
Mini Clock pulse low period	T_3		1.6				
Mini Data setup time	T_5		0.55				
Mini Data hold time	T_6		0.55				

Note: 1. In case of mini-LVDS signal spec, refer to FIG 1-1 for the more detail.

2. Mini-LVDS timing measure conditions

: $120\text{MHz} < \text{Clock Frequency} < 156\text{MHz}$, $200\text{mV} < VID < 700\text{mV}$

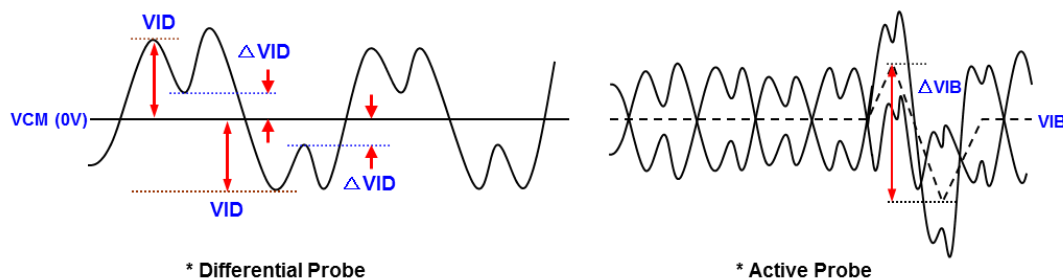


FIG 1-1. Description of VID, ΔV_{IB} , ΔV_{ID}

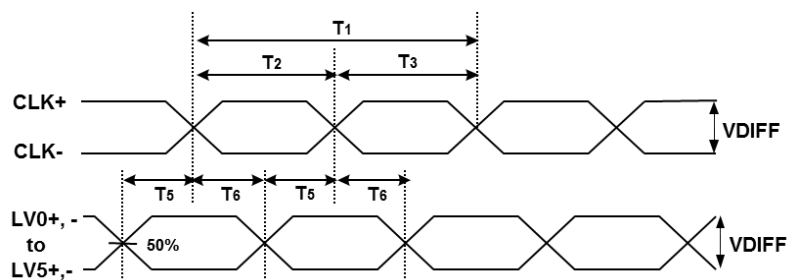


FIG 1-2. Source D-IC Input Data Latch Timing Waveform

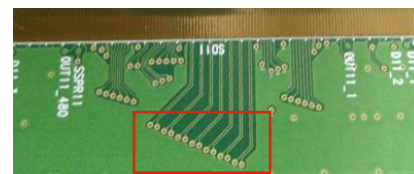


FIG 1-3. Measure point

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3-6. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10bit gray scale data input for the color. The higher binary input, the brighter the color. Table 6 provides a reference for color versus data input.

Table 6. COLOR DATA REFERENCE

Color		Input Color Data																													
		RED										GREEN										BLUE									
		MSB									LSB	MSB									LSB	MSB									LSB
	R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0	
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red (1023)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green (1023)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
RED	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (001)	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
										
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
										
	GREEN (1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
BLUE	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
										
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

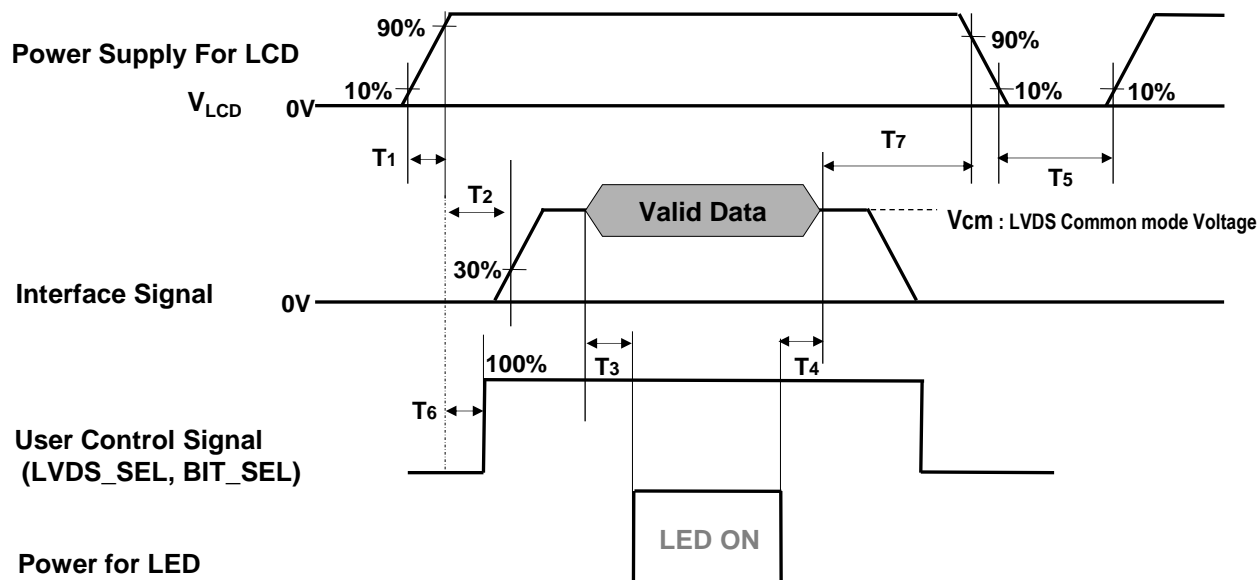


Table 8. POWER SEQUENCE

Parameter	Value			Unit	Note
	Min	Typ	Max		
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
T3	400	-	-	ms	3
T4	100	-	-	ms	3
T5	1.0	-	-	s	4
T6	0	-	T2	ms	5
T7	0	-	-	ms	6

- Note
1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 4. T5 should be measured after the Module has been fully discharged between power off and on period.
 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
 6. It is recommendation specification that T7 has to be 0ms as a minimum value.
 - ※ Please avoid floating state of interface signal at invalid period.
 - ※ When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm 2^{\circ}\text{C}$. The values are specified at 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° . FIG. 2 shows additional information concerning the measurement equipment and method.

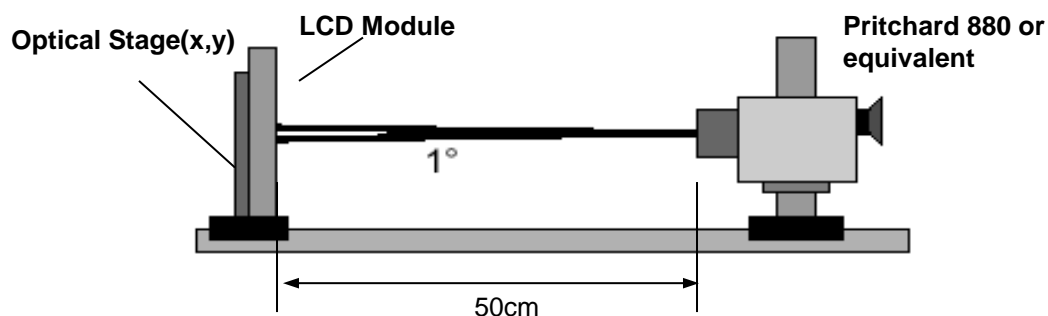


FIG. 2 Optical Characteristic Measurement Equipment and Method

$T_a = 25\pm 2^{\circ}\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 60\text{Hz}$, $D_{\text{clk}} = 74.25\text{MHz}$,
Light Source : D65 Standard

Table 8. OPTICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note	
		Min	Typ	Max			
Contrast Ratio	CR	900	1300	-		1	
Response Time	Gray to Gray	G to G	8	12		3	
	Uniformity	$\delta_{\text{G TO G}}$	-	1	ms	2	
Transmittance	T	8.55	9.5		%	4	
Color Coordinates [CIE1931]	RED	Rx	Typ -0.03	0.637	Typ +0.03	5	
		Ry		0.331			
	GREEN	Gx		0.311			
		Gy		0.599			
	BLUE	Bx		0.155			
		By		0.063			
Viewing Angle (CR>10)							
	x axis, right($\phi=0^{\circ}$)	θ_r	89	-	-	degree	6
	x axis, left ($\phi=180^{\circ}$)	θ_l	89	-	-		
	y axis, up ($\phi=90^{\circ}$)	θ_u	89	-	-		
	y axis, down ($\phi=270^{\circ}$)	θ_d	89	-	-		
Gray Scale			-	-	-		7

Product Specification

Note : 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center 1-point.

The contrast ratio is valued with operating condition of LGD's standard BLU

2. Response time is the time required for the display to transit from Gray(N) to Gray(M) (Rise Time, Tr_R) and from Gray(M) to Gray(N) (Decay time, Tr_D). For additional information see the FIG. 3.

※ G to G Spec stands for average value of all measured points.

Photo Detector : RD-80S / Field : 2 °

The response time is valued with operating condition of LGD's standard BLU

3. The variation of G to G Uniformity , δ G to G is defined as :

$$\text{G to G Uniformity} = \frac{\text{Maximum}(GtoG) - \text{Typical}(GtoG)}{\text{Typical}(GtoG)} \leq 1$$

*Maximum (G to G) means maximum value of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).

4. The value of transmittance should be extracted using the standard light source of D65

5. The value of color coordinates should be extracted using the standard light source of D65

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.

7. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 9.

Table 9. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
L0	0.076
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

Product Specification

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

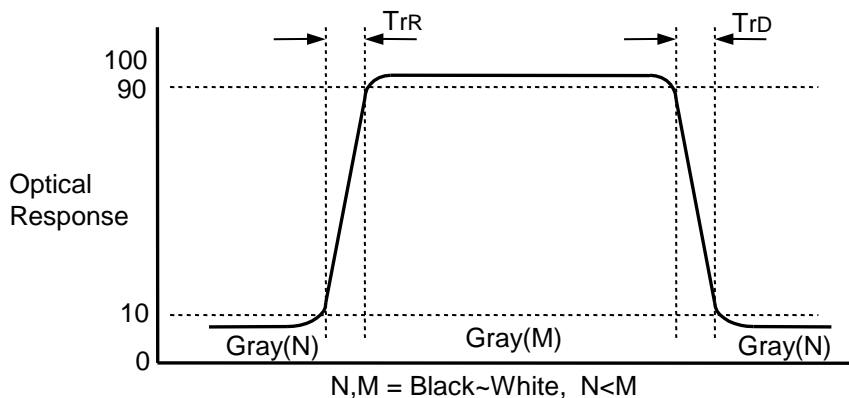


FIG. 3 Response Time

Dimension of viewing angle range

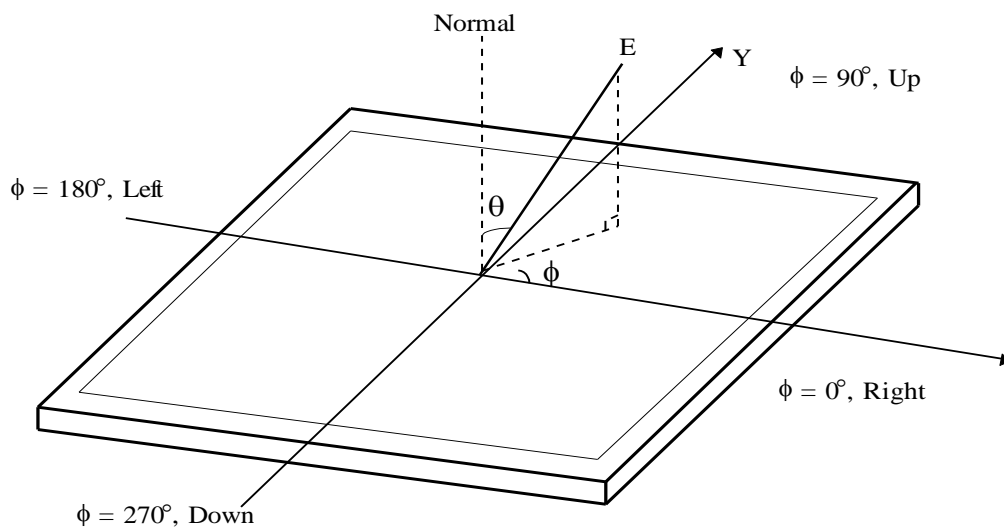


FIG. 4 Viewing Angle

Product Specification

5. Mechanical Characteristics

Table 10 provides general mechanical characteristics.

Table 10. MECHANICAL CHARACTERISTICS

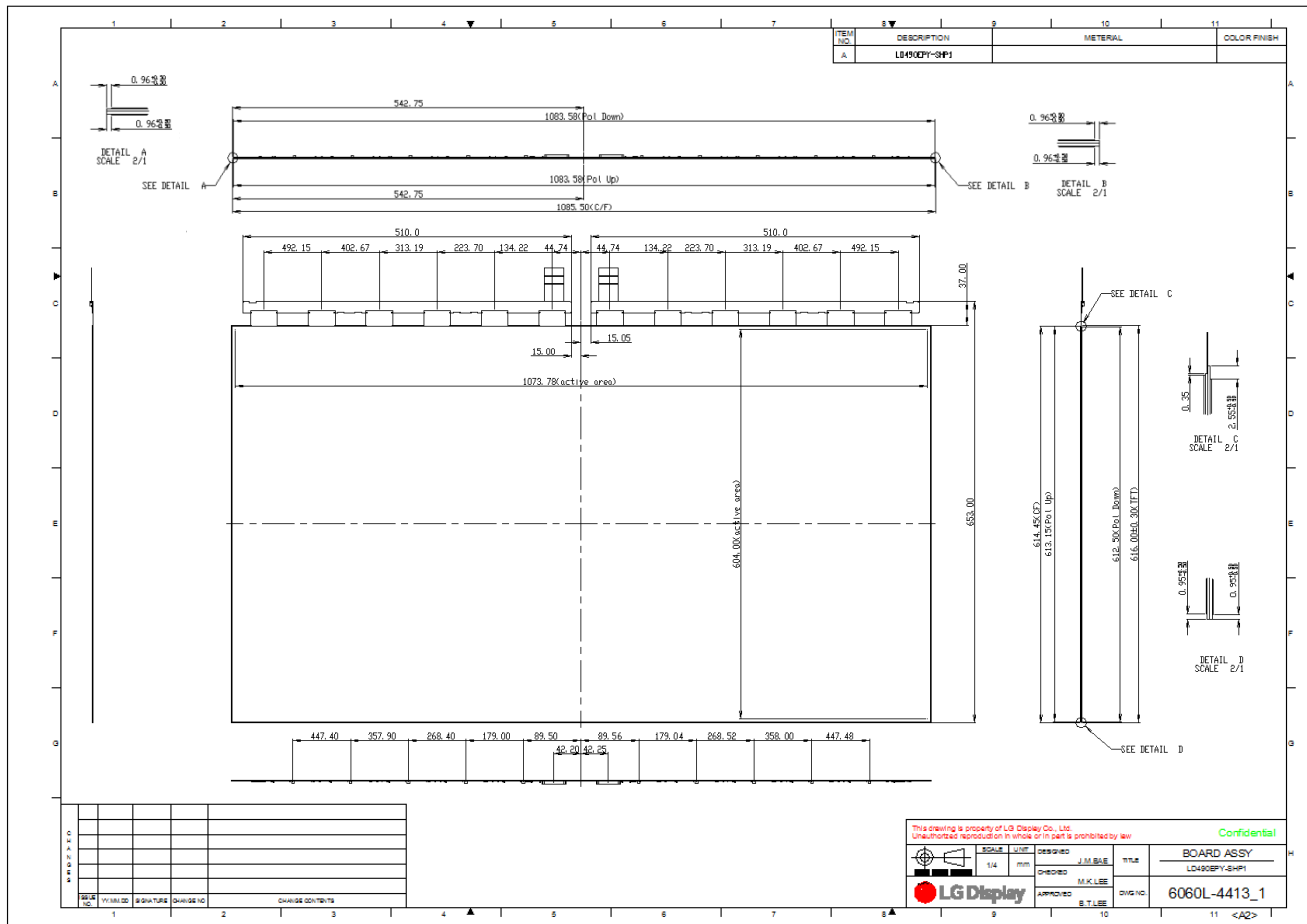
Item	Value	
Outline Dimension (Only Glass)	Horizontal	1085.5mm
	Vertical	616.0mm
	Thickness	1.3mm
Active Display Area	Horizontal	1073.8mm
	Vertical	604.0mm
Weight	2,600g(Typ.), 2,730g(Max)	
Surface Treatment	Hard coating(2H), Anti-reflection treatment of the front polarizer (Reflectance < 2%)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

Product Specification

6. Mechanical Dimension

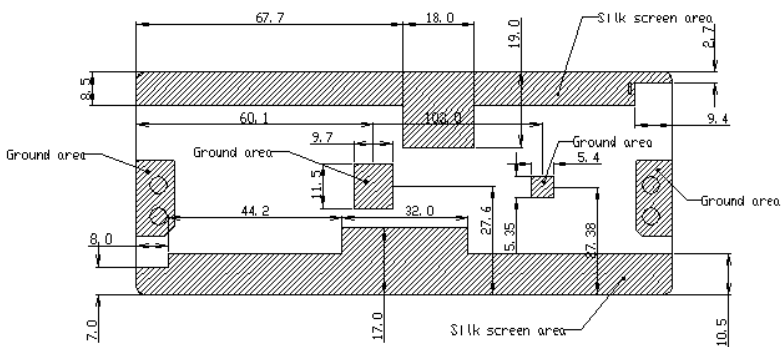
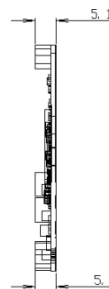
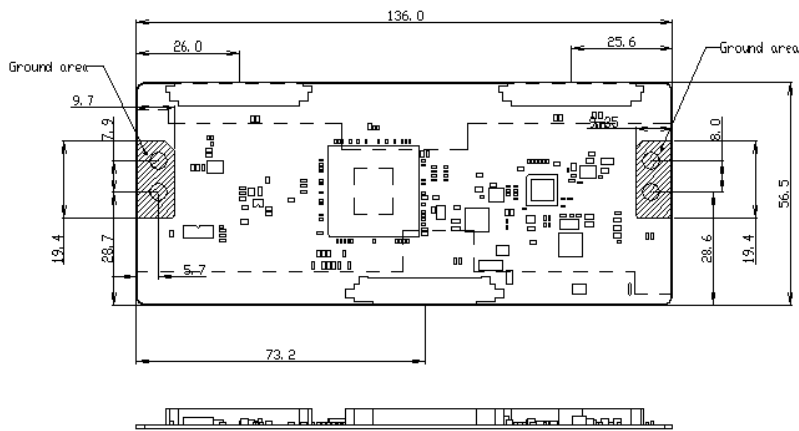
6-1. Board Assembly Dimension



Date	Version
15.03.19	1.0

Product Specification

6-2. Control Board Assembly Dimension



NOTE
 1. This drawing describes the necessary dimension in control board to assembly with customer's backlight system.
 2. The specified dimension non-specified dimension and its tolerances can be changeable, and hence values are used for reference only.
 3. Hatching area describes as below
 ; Ground area
 ; Silk screen area

Date	Version
15.03.19	1.0
15.07.14	1.1

Product Specification

7. Reliability**Table 11. ENVIRONMENT TEST CONDITION**

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 90% 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 500h
4	Low temperature operation test	Ta= 0°C 500h
5	Humidity condition Operation	Ta= 40 °C ,90%RH
6	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

8. International Standards

8-1. Safety

- a) UL 60065, Underwriters Laboratories Inc.
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association.
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) IEC 60065, The International Electrotechnical Commission (IEC).
Audio, Video and Similar Electronic Apparatus - Safety Requirements.

8-2. Environment

- a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011

Product Specification

9. Packing**9-1. Packing Form**

- a) Package quantity in one Pallet : 120 pcs
- b) Pallet Size : 1250 mm(W) X 800 mm(D) X 1105 mm(H)

Product Specification

10. Precautions

Please pay attention to the followings when you use this TFT LCD module.

10-1. Handling Precautions

- (1) Please attach the surface transparent protective film to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (2) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (3) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (4) After removing the protective film, when the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (5) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (6) Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly. Panel ground path should be connected to metal ground.
- (7) Please make sure to avoid external forces applied to the Source PCB and D-IC during the process of handling or assembling the TV set. If not, it causes panel damage or malfunction.
- (8) Panel and BLU should be protected from the static electricity. If not, it causes IC damage.
- (9) Do not pull or fold the source D-IC which connect the source PCB and the panel.
- (10) Panel(board ass'y) should be put on the BLU structure precisely to avoid mechanical impact.
- (11) FFC Cable should be connected between System board and Source PCB correctly.
- (12) Mechanical structure for backlight system should be designed for sustaining board ass'y safely.
- (13) Surface temperature of the Source D-IC should be controlled under 100°C with TV Set status. If not, problems such as IC damage or decrease of lifetime could occur.

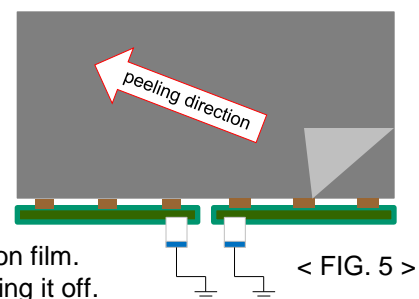
10-2. Operating Precautions

- (1) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, Stable time (required time that brightness is stable after turned on) becomes longer
- (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

Product Specification

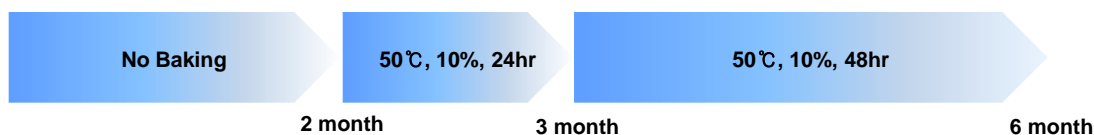
10-3. Protection Film

- (1) Please keep attaching the protection film before assembly.
- (2) Please peel off the protection film slowly.
- (3) Please peel off the protection film just like shown in the FIG. 5
- (4) Ionized air should be blown over during the peeling.
- (5) Source PCB should be connected to the ground when peel off the protection film.
- (6) The protection film should not be contacted to the source D-IC during peeling it off.

**10-4. Storage Precautions**

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Temperature : 5 ~ 40 °C
- (2) Humidity : 35 ~ 75 %RH
- (3) Period : 6 months
- (4) Control of ventilation and temperature is necessary.
- (5) Please make sure to protect the product from strong light exposure, water or moisture. Be careful for condensation.
- (6) Please keep the modules at a circumstance shown below FIG. 6



< FIG. 6 >

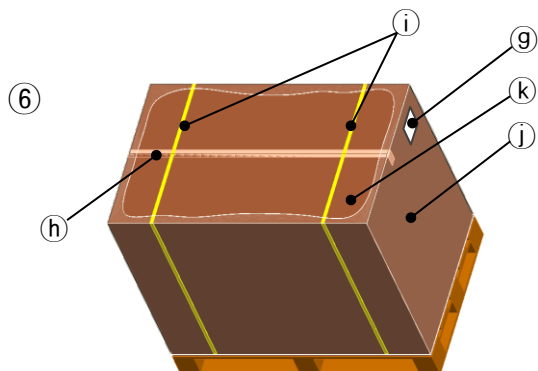
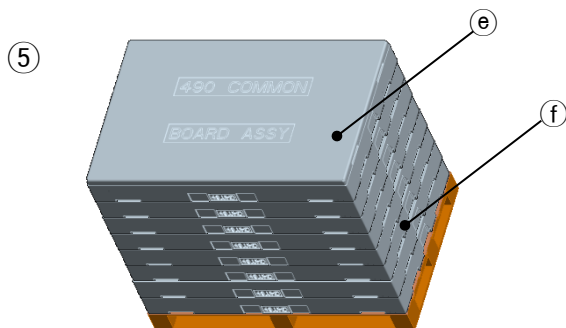
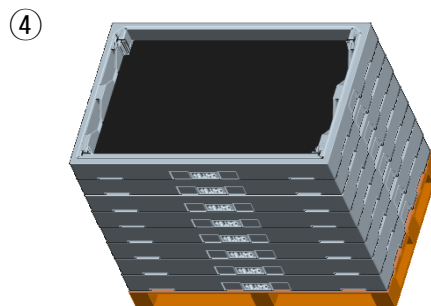
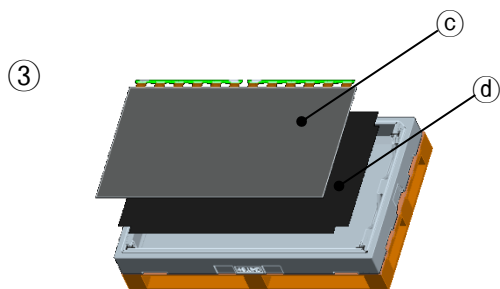
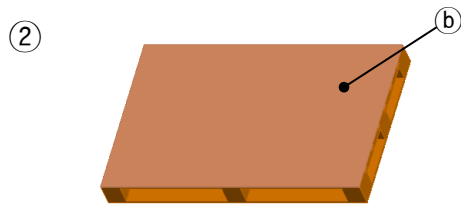
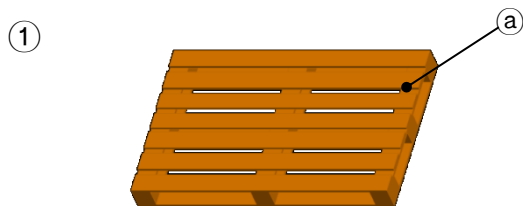
10-5. Packing Precautions

Product assembled into module should be stored in the Al-bag(cover case).

Product Specification

APPENDIX-I-1

■ Pallet Ass'y



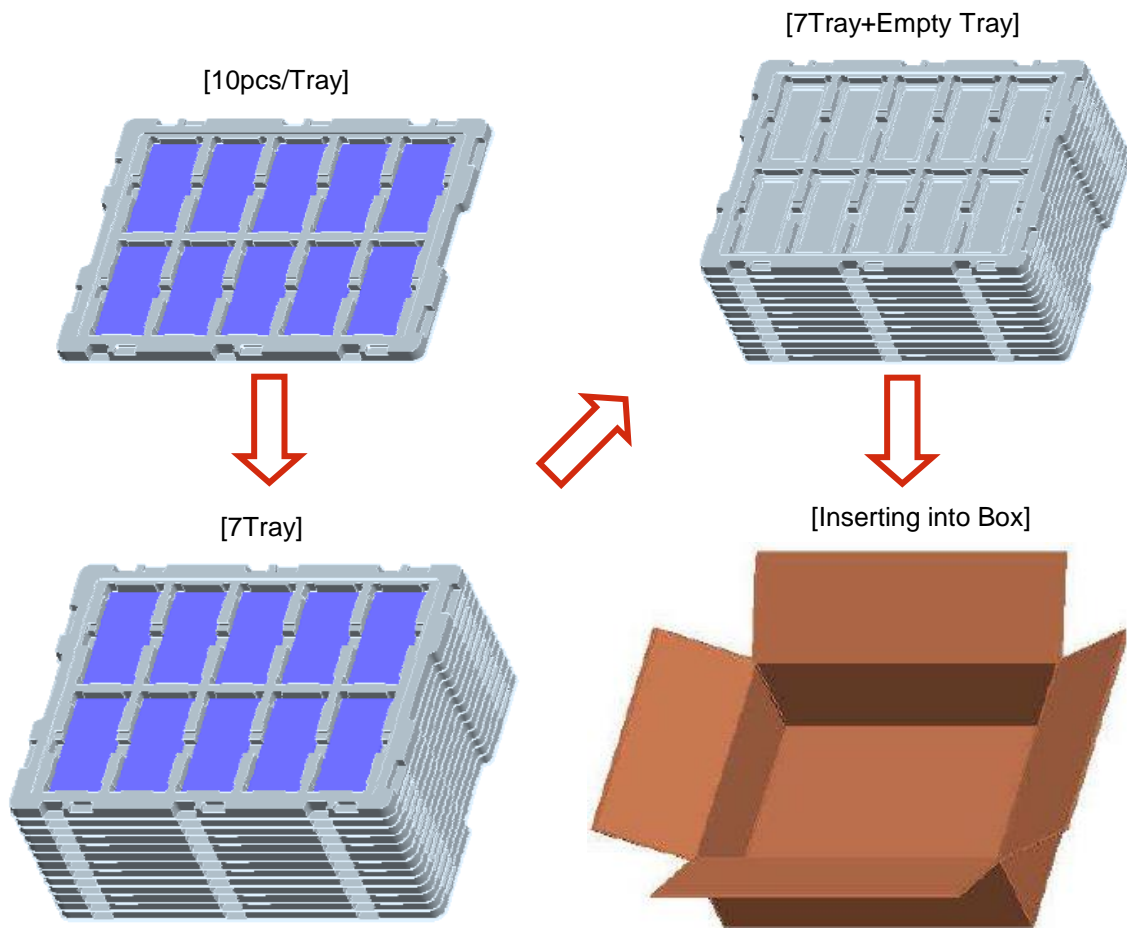
No.	Description	Material
(a)	Pallet	Plywood
(b)	Carton Plate	Paper(SW)
(c)	Board Ass'y	-
(d)	PE Sheet	LDPE
(e)	Top Packing	EPS
(f)	Bottom Packing	EPS
(g)	Angle Packing	Paper(SW)
(h)	Tape	OPP
(i)	Band	PP
(j)	Wrap	L-LDPE
(k)	Label	PP

Product Specification

APPENDIX-I-2

■ Control PCB Packing Ass'y

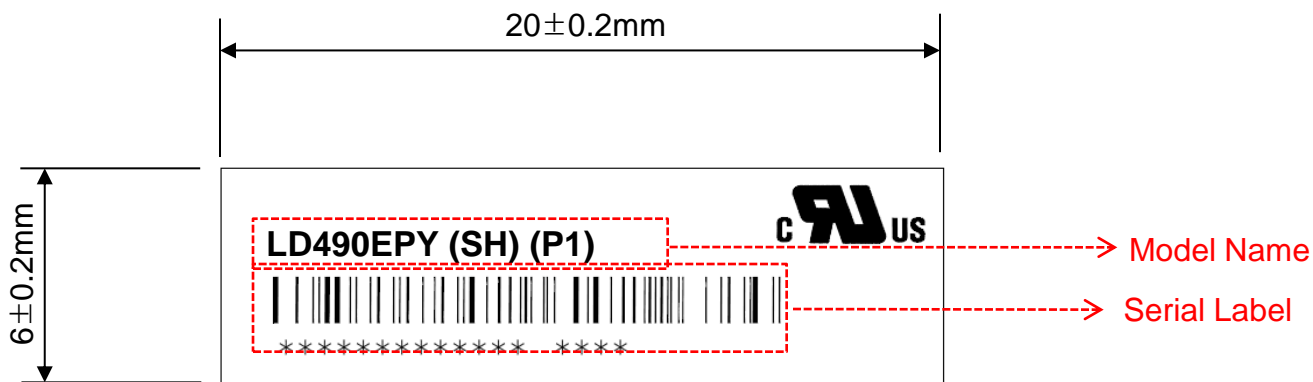
- a) Control PCB Qty / Box : 70 pcs
- b) Tray Qty / Box : 8Tray(Upperst Tray Is empty)
- c) Tray Size : 453 X 353 X 16
- d) Box size : 468 X 355 X 98



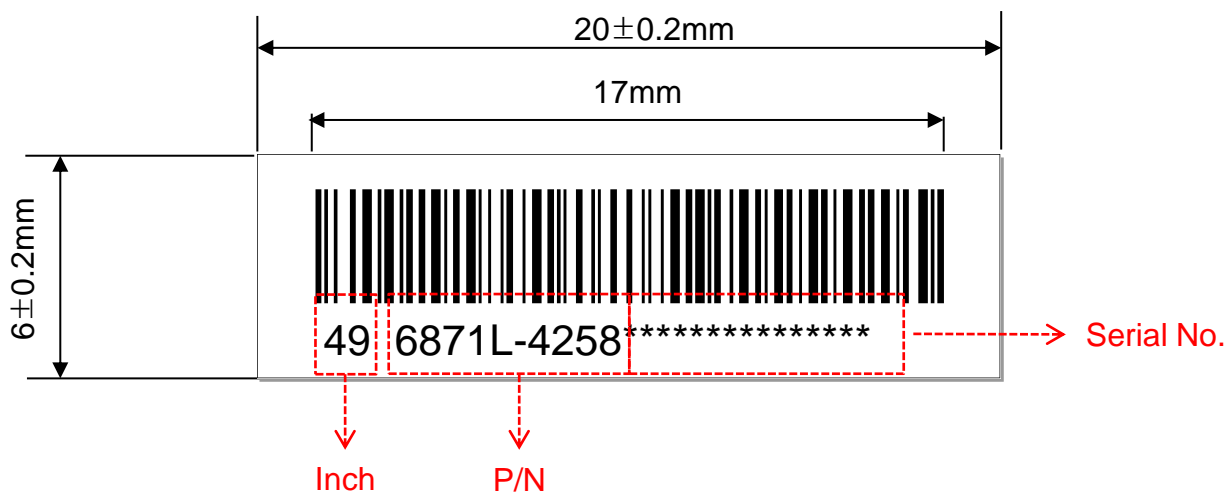
NO.	DESCRIPTION	MATERIAL
1	PCB Packing A,ssy	-
2	Tray	PET
3	Box	SWR4

APPENDIX- II-1

■ Serial Label



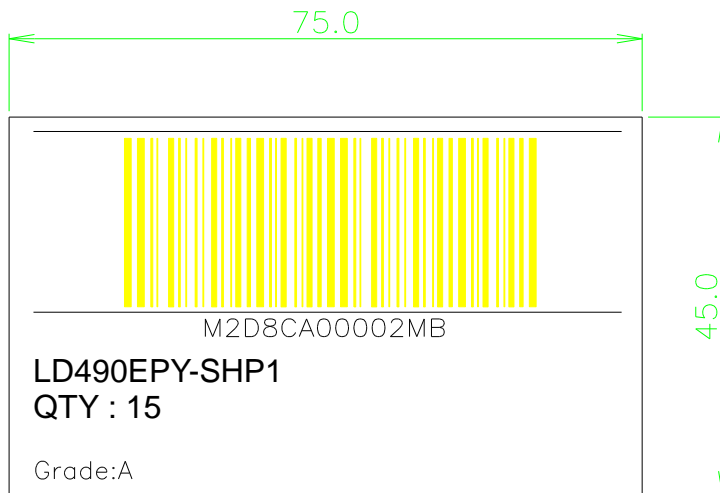
■ Control PCB Label



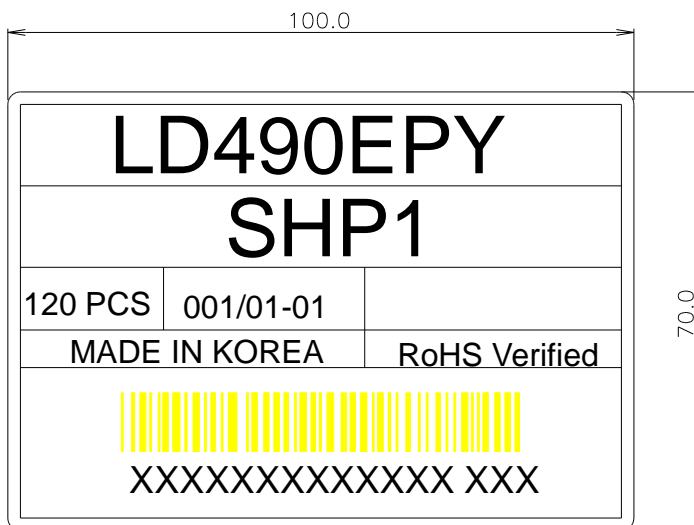
Product Specification

APPENDIX- II-2

■ BOX Label



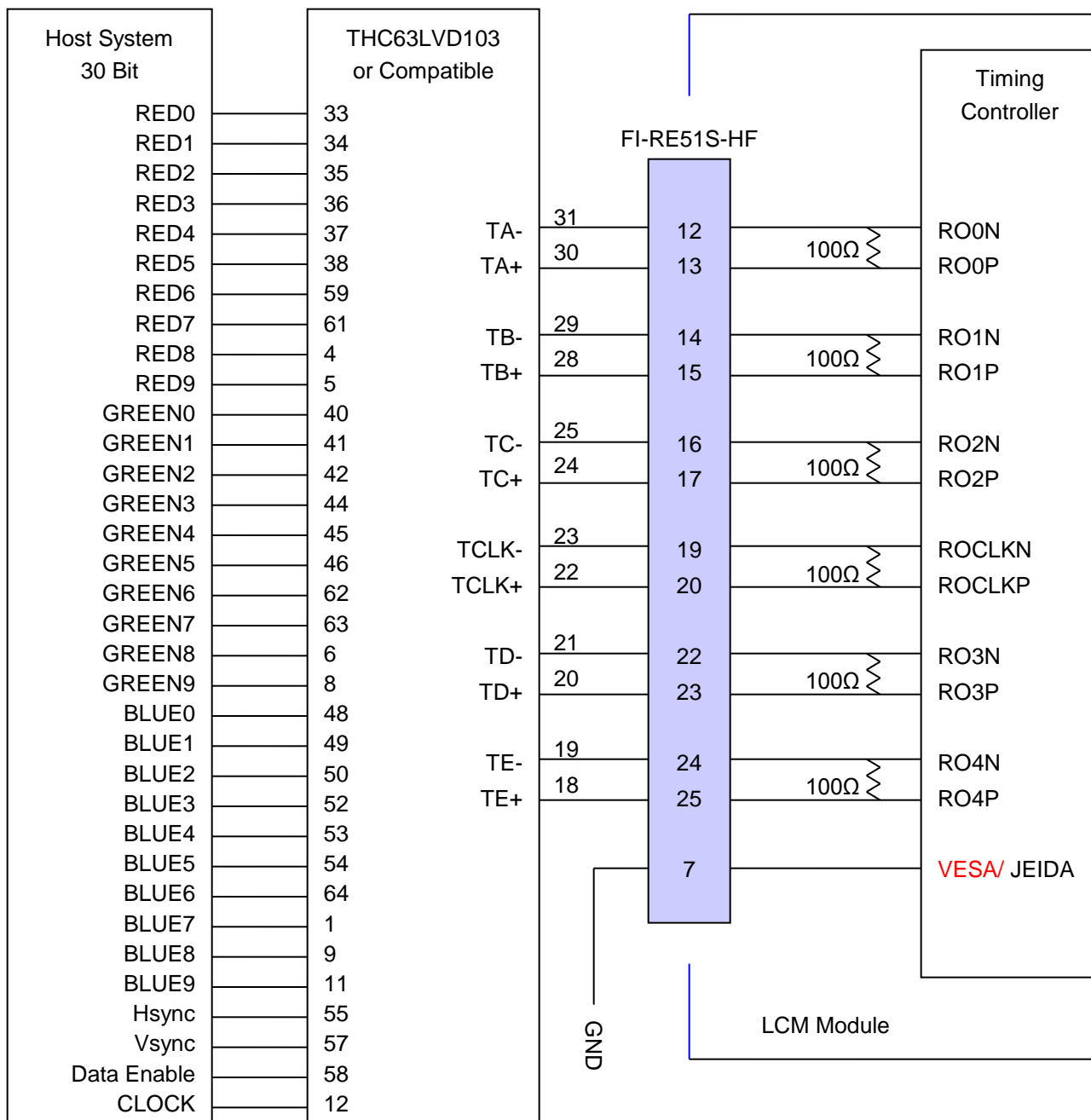
■ Pallet Label



Product Specification

APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "L" or "NC")



Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

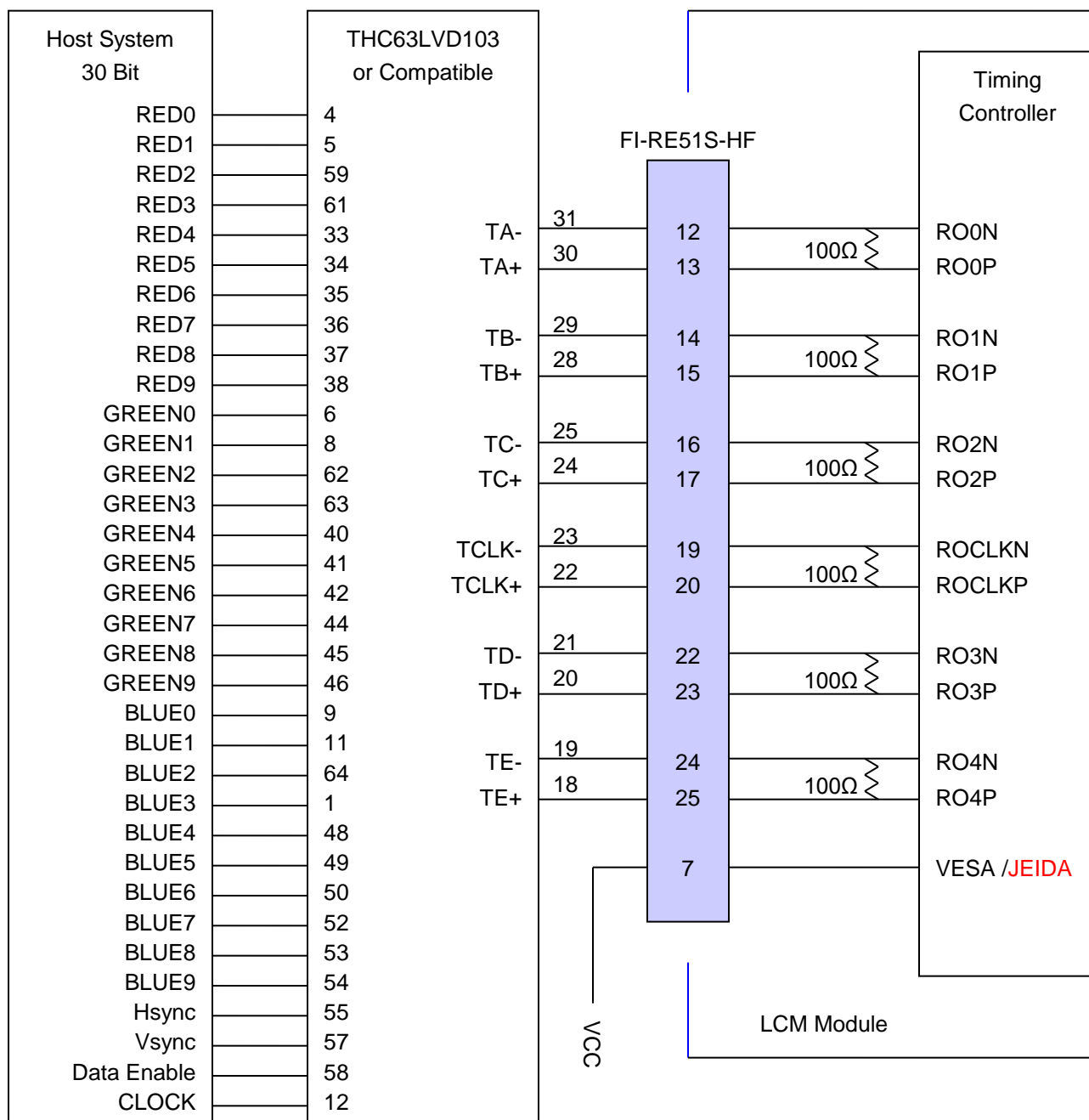
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '9' means MSB and '0' means LSB at R,G,B pixel data.

Product Specification

APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "H")



Note :1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

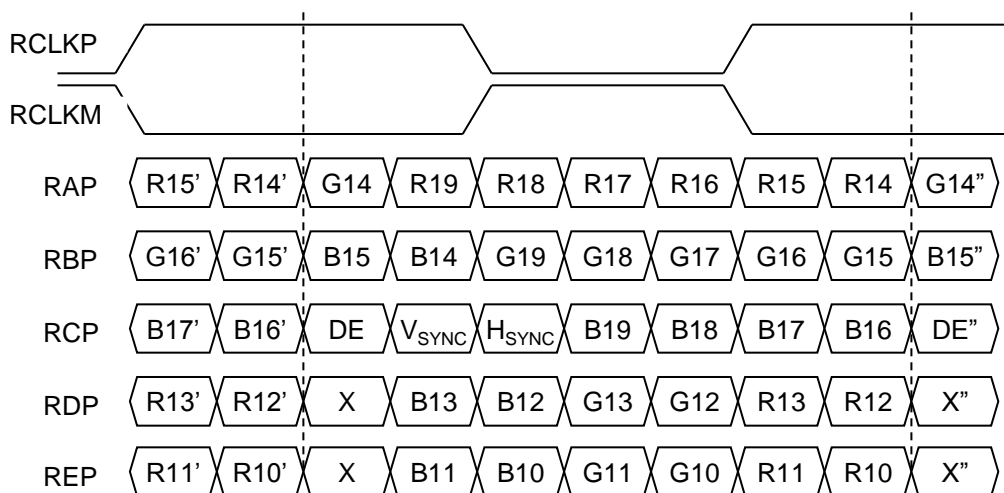
3. '9' means MSB and '0' means LSB at R,G,B pixel data.

Product Specification

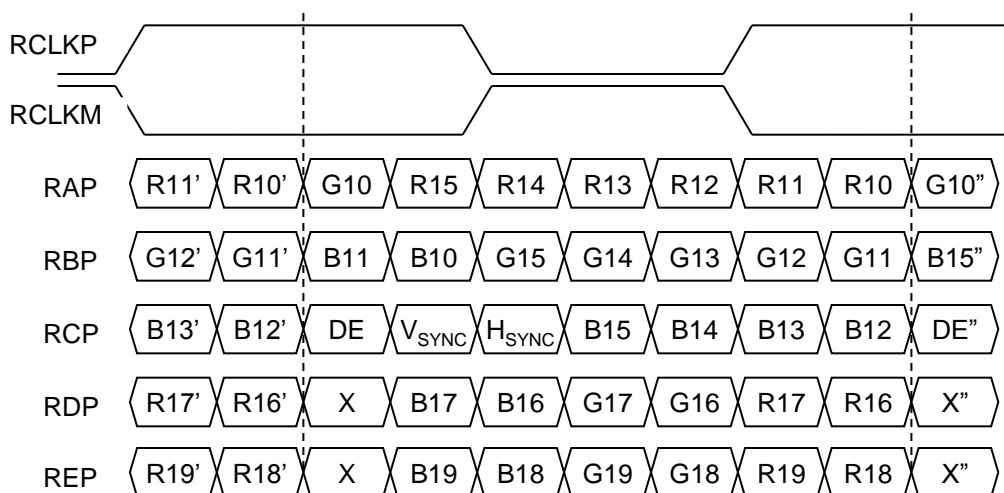
APPENDIX- IV-1

■ LVDS Data-Mapping Information (10 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



2) LVDS Select : "L" Data-Mapping (VESA format)

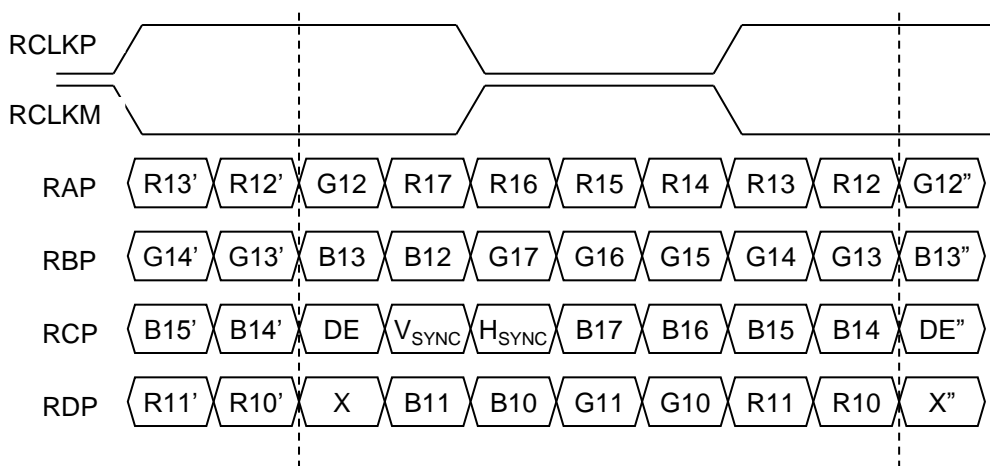


Product Specification

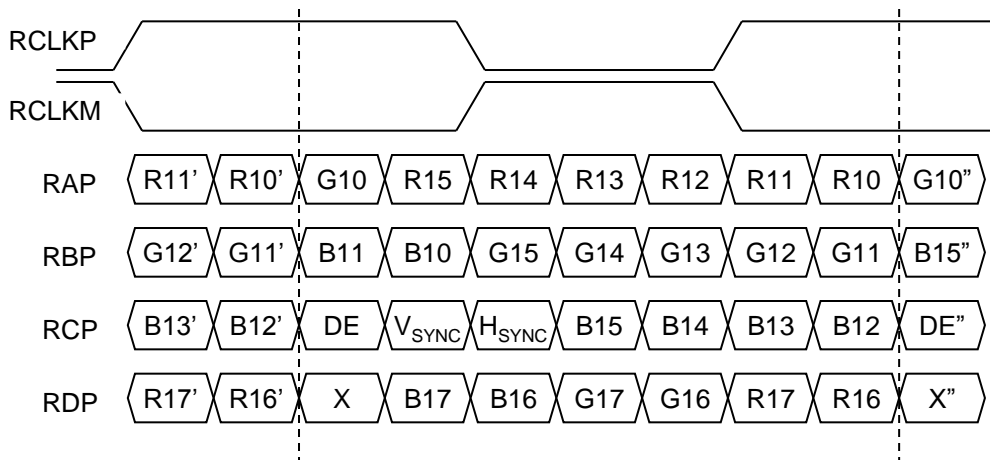
APPENDIX- IV-2

■ LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



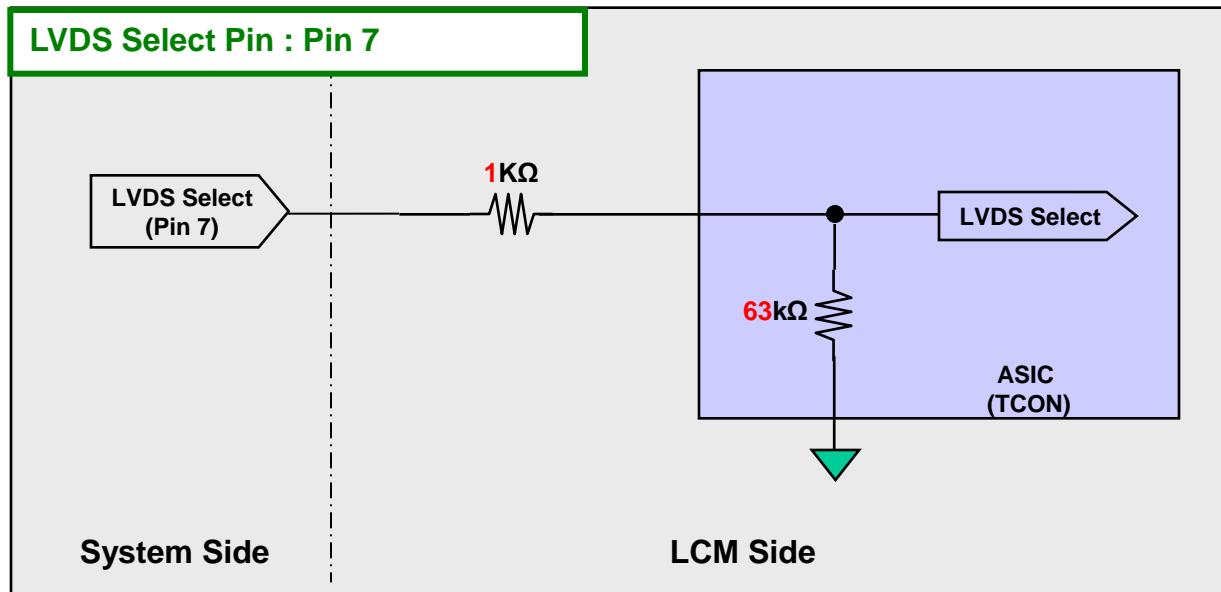
2) LVDS Select : "L" Data-Mapping (VESA format)



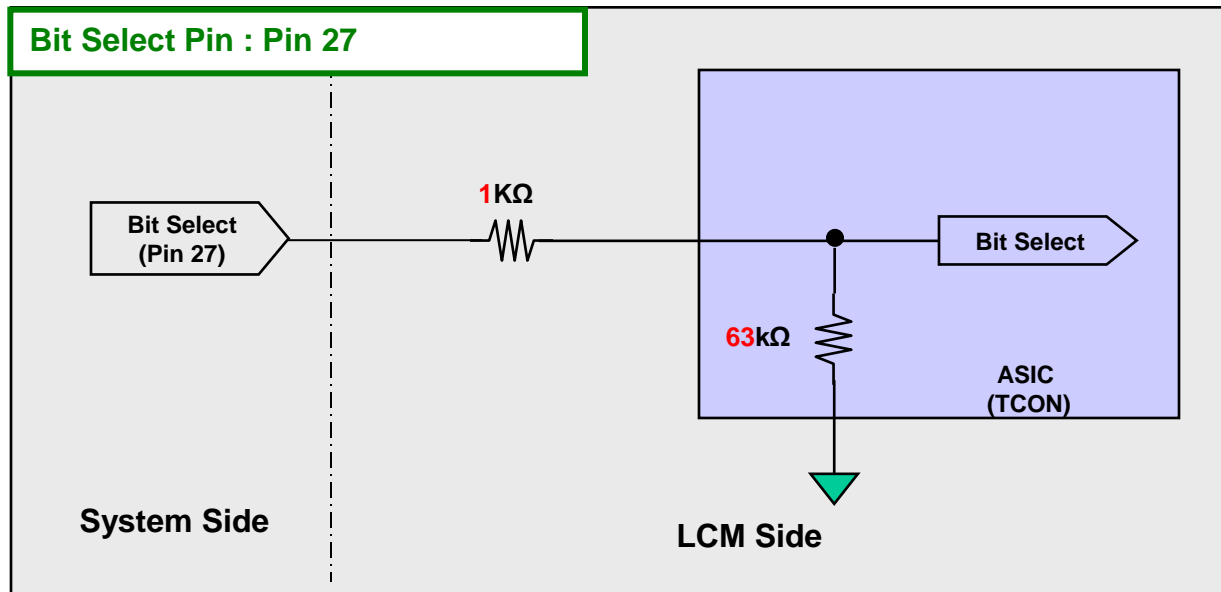
APPENDIX- V

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of Bit Selection pin



APPENDIX-VI

■ The reference method of BL dimming

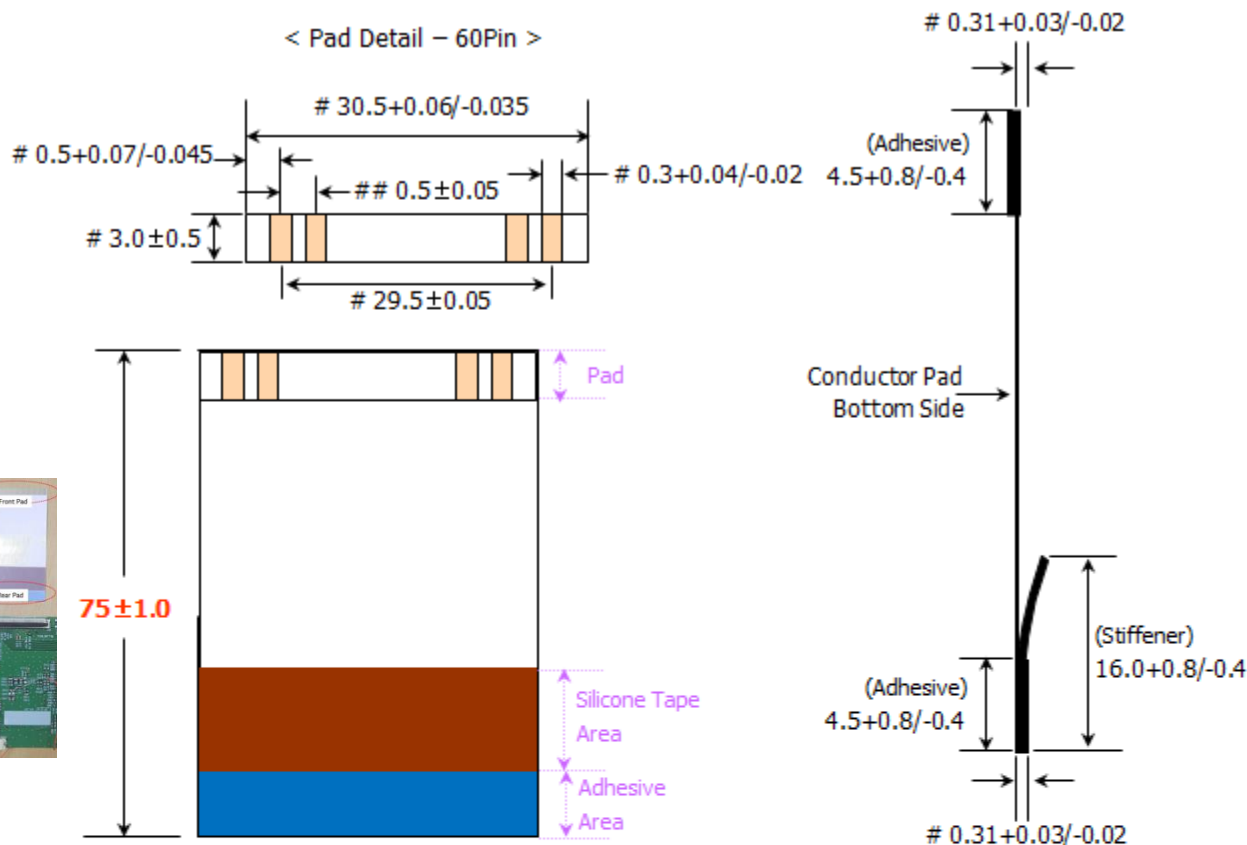
It is recommended to use synchronous V-sync frequency to prevent waterfall
(Vsync * 2 =P-Dim Frequency)

Product Specification

APPENDIX-VII

-LCD Connector : GF05G-60S-E2000 (Manufactured by LS CABLE) or Compatible

< Pad Detail - 60Pin >



75±1.0

Pad

Silicone Tape Area

Adhesive Area

Conductor Pad
Bottom Side

(Stiffener)

(Adhesive)

0.31±0.03/-0.02

Adhesive - left

Adhesive - right

0.3 mm Max

◆ Note

- Pad : GOLD Plating
- #치수 : Cpk 1.0 이상
- ##치수 : Cpk 1.33 이상
- Stiffener 색상 : Sky Blue
(Silicone Tape 색상 : Brown)
- H-F
- 치수 단위 : mm

MATERIAL LIST

APPLICATION	STANDARD	MATERIAL	REFERENCE
1. CONDUCTOR(도체)	GOLD PLATED COPPER WIRE	1. 99.99% COPPER 2. GOLD PLATED	1. Tolerance - THICKNESS : ±0.01 - WIDTH : ±0.04 / -0.02 2. ELONGATION(%) : 10 MORE THAN 3. TENSION : (GGF) 0.2 MORE THAN 4. GOLD THICKNESS : 0.05 μm MIN *Weight : 1.348g *MAKER : LS CABLE *PLATING PROCESS : SUNSUN
2. INSULATION(절연체)	POLYESTER FILM (PET/PET)	1. POLYESTER BASE FILM : 0.025 mm 2. POLYESTER HOTMELT ADHESIVE : 0.005 mm TOTAL THICKNESS : 0.060 mm	UL VW-1 FLAME Width : 120mm Length : 500M UNIT : ROLL Weight : 0.3324g *MAKER : COSMOAMT/SHENCHANG HOTMELT
3. SUPPORTING TAPE(보강편)	POLYESTER FILM	1. POLYESTER BASE FILM : 0.188 mm 2. POLYESTER HOTMELT ADHESIVE : 0.027 mm TOTAL THICKNESS : 0.215 mm	Width : 20.5mm Length : 250M Unit : ROLL Weight : 0.177g *MAKER : SUNGSHIN Trading/ COSMOAMT
4. SILICON TAPE (BROWN) 실리콘 테이프		TOTAL THICKNESS : 0.065 mm	Weight : 0.01g *MAKER : DAEDHYUN ST